## Notice of References Cited

Application/Control No.

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Examiner

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Reexamination
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Art Unit
Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,247,157 B1	06-2001	Edirisooriya, Samantha J. W.	714/767
	В	US-6,158,017	12-2000	Han et al.	714/6
	C	US-5,579,475	11-1996	Blaum et al.	714/7
	D	US-5,537,567	07-1996	Galbraith et al.	711/114
	E	US-			·
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-		·	
	К	US-		·	
	L	US-			
	М	US-			

## **FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	ø			-		
	R					
	S					
	T					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	υ	Lee et al., Double parity sparing for improvement of performance and reliability in disk arrays, 1996, IEEE, gp. 345-348.					
	V						
	w						
	х						

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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